

U.S. Department of Commerce, Patent & Trademark Office	Application No.: 09/779,381
	Filing date: February 7, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Parruck, et al.
(Use several sheets if necessary)	Group Art Unit: 2816
"Multiservice Network Processing Device"	Examiner name: unknown
	Atty. Docket: AZA-001/2001-P001

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
GBS	AA	5,666,353	09/09/97	Klausmeier et al.	370	230	03/21/95
GBS	AB	5,742,765	04/21/98	Wong et al.	395	200	06/19/96
GBS	AC	5,787,255	07/28/98	Parlan et al.	395	200	04/12/96
GBS	AD	5,802,052	09/01/98	Venkataraman	370	395	01/26/96
GBS	AE	5,802,287	09/01/98	Rostoker et al.	395	200	08/03/95
GBS	AF	5,809,021	09/15/98	Diaz et al.	370	364	06/10/96
GBS	AG	5,838,915	11/17/98	Klausmeier et al.	395	200	11/17/97
GBS	AH	5,859,835	01/12/99	Varma et al.	370	229	04/15/96
GBS	AI	5,867,480	02/02/99	Thomas et al.	370	230	09/12/96
GBS	AJ	5,870,394	02/09/99	Oprea	370	392	07/23/96
GBS	AK	5,898,688	04/27/99	Norton et al.	370	362	05/24/96
GBS	AL	5,905,725	05/18/99	Sindhu et al.	370	389	04/18/97
GBS	AM	5,917,820	06/29/99	Rekhter	370	392	06/10/96
GBS	AN	5,917,828	06/29/99	Thompson	370	474	01/03/97
GBS	AO	5,920,705	07/06/99	Lyon et al.	395	200	01/30/97
GBS	AP	5,946,313	08/31/99	Allan et al.	370	397	03/20/97
GBS	AQ	5,963,541	10/05/99	Koga et al.	370	229	11/09/95
GBS	AR	5,987,034	11/16/99	Simon et al.	370	465	08/30/96
GBS	AS	6,026,086	02/15/00	Lancelot et al.	370	353	01/08/97
GBS	AT	6,034,945	03/07/00	Hughes et al.	370	230	05/15/96
GBS	AU	6,052,379	04/18/00	Iverson et al.	370	442	09/18/98
GBS	AV	6,088,355	07/11/00	Mills et al.	370	392	10/11/96
GBS	AW	6,091,725	07/18/00	Cheriton et al.	370	392	12/29/95
GBS	AX	6,101,188	08/08/00	Sekine et al.	370	401	09/10/97
GBS	AY	6,111,877	08/29/00	Wilford et al.	370	392	12/31/97

Examiner

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Date Considered

10/25/04

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GBS	BA 6,134,217	10/17/00	Stiliadis et al.	370	232	04/16/96
GBS	BB 6,144,662	11/07/00	Colmant et al.	370	390	07/09/96
GBS	BC 6,147,999	11/14/00	Honda et al.	370	396	05/27/99
GBS	BD 6,154,446	11/28/00	Kadambi et al.	370	239	06/30/99
GBS	BE 6,157,641	12/05/00	Wilford	370	389	08/22/97
GBS	BF 6,160,793	12/12/00	Ghani et al.	370	236	10/13/98
GBS	BG 6,167,027	12/26/00	Aubert et al.	370	230	09/23/97
GBS	BH 6,169,739	01/02/01	Isoyama	370	395	01/08/98
GBS	BI 6,175,570	01/16/01	Cukier et al.	370	414	12/04/96
GBS	BJ 6,185,212	02/06/01	Sakamoto et al.	370	395	04/08/98
GBS	BK 6,188,671	02/13/01	Chase et al.	370	232	12/10/98

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
GBS	BL	WO 97/43869	11/20/97	international	H04Q			
GBS	BM	WO 99/17182	04/08/99	international	G06F			
GBS	BN	WO 00/56012	09/21/00	international	H04L			

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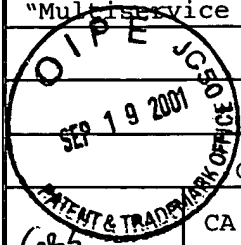
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
685	CA	Application 09/528,802, filed 03/20/00, "Circuits for Combining ATM and Packet Data on an Optical Fiber and Methods Therefor," by Parruck, Nguyen, Ramakrishnan
685	CB	Application 09/539,479, filed 03/30/00, "Devices for Aggregating ATM Cells and Packets for Transmission Over the Same Channel," by Parruck, Nguyen, Ramakrishnan
685	CC	Application 09/539,306, filed 03/30/00, "Devices for Employing SONET Frames for Transporting both ATM Cells and Packets Over in the Same Channel in an Optical Fiber," by Parruck, Nguyen, Ramakrishnan
685	CD	Application 09/539,478, filed 03/30/00, "Methods and Apparatus for Dynamically Allocating Bandwidth Between ATM Cells and Packets," by Parruck, Nguyen, Ramakrishnan
685	CE	Application 09/539,461, filed 03/30/00, "Routers for Switching ATM Cells in a Packet-like manner Using a Packet Switch," by Parruck, Nguyen, Ramakrishnan
685	CF	Application 09/539,476, filed 03/30/00, "Processor-based Architecture for Facilitating Integrated Data Transfer between both ATM and Packet Traffic with a Packet Bus or Packet Link, Including Bidirectional ATM-to-Packet Functionality for ATM Traffic," by Parruck, Nguyen, Ramakrishnan
685	CG	Application 09/539,477, filed 03/30/00, "Integrated ATM/Packet Segmentation-and-Reassembly Engine for Handling both Packet and ATM Input Data and for Outputting both ATM and Packet Data," by Parruck, Nguyen, Ramakrishnan
685	CH	"Is the Bloom Off Jasmine" and other pages from 'Light READING' web site "www.lightreading.com", 2 pages (printed 03/26/01).
685	CI	Chapter 3, Frame Relay Architecture, from "Frame Relay Technology and Practice" textbook, by Jeff T. Buckwalter, (copyright 2000).
685	CJ	Chapter 5, MPLS Core Protocols, from "MPLS Technology and Applications" textbook, by Bruce Davis and Yakov Rekhter, (copyright 2000).
685	CK	"GenFlow Family of Traffic Management Co-Processors" and other pages from Acorn Networks' web site "www.Acorn-Networks.com", 5 pages (printed 3/30/01).
685	CL	White Paper entitled "Wire-Speed Traffic Management Solutions Based On Coprocessors", by Joe Adler and Tam-Anh Chu, Acorn Networks, 14 pages (copyright 2000).
685	CM	S19202 Advanced Summary Datasheet for "Ganges STS-192 POS/ATM Sonet/SDH Mapper", Applied Micro Circuits Corporation, revision 1.6, 2 pages (June 2000).
685	CN	"Product Guide - Ganges (S19202)" and other pages from Applied Micro Circuits Corporation's web site, 3 pages (printed 2/27/01).
685	CO	"ZettaCom - Powering Tomorrow's Network", pages from ZettaCom's web site "www.zettacom.com/products", 2 pages (downloaded 1/31/01).
685	CP	"Ocular Networks' Metro Business Access Architecture, pages from Ocular Networks' web site "www.ocularnetworks.com/architecture", two pages (downloaded 1/31/01).
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